

Form PTO-1449 (modified)Atty. Docket No.
SILA:097Serial No.
10/075,099

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PATENT & TRADEMARK OFFICE
List of Patents and Publications for Applicant's
INFORMATION DISCLOSURE STATEMENT
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Applicants
TOD PAULUS ET AL.Filing Date:
2/12/02Group:
2681U.S. Patent Documents
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See Pages 3-10**U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
J.L	A1	5,828,955	10/27/98	Lipowski et al.			8/30/95
J.L	A2	6,035,186	3/7/00	Moore et al.			3/11/97
J.L	A3	6,075,979	6/13/00	Holtvoeth et al.	RECEIVED DEC 04 2002	Technology Center 2600	3/5/97
J.L	A4	5,764,171	6/9/98	Stikvoort			4/2/96
J.L	A5	6,148,048	11/14/00	Kerth et al.			9/26/97
J.L	A6	4,713,563	12/15/87	Marshall et al.			5/12/86
J.L	A7	4,070,632	1/24/78	Tuttle			9/22/76
J.L	A8	4,236,252	11/25/80	Kominami et al.			2/6/79
J.L	A9	4,680,588	7/14/87	Cantwell			12/5/85
J.L	A10	4,857,928	8/15/89	Gailus et al.			1/28/88
J.L	A11	4,989,074	1/29/91	Matsumoto			9/21/89
J.L	A12	5,050,192	9/17/91	Nawata			11/21/90
J.L	A13	5,083,304	1/21/92	Cahill			9/28/90
J.L	A14	5,142,695	8/25/92	Roberts et al.			3/21/91
J.L	A15	5,194,826	3/16/93	Huusko			4/12/91
J.L	A16	5,235,410	8/10/93	Hurley			7/10/91
J.L	A17	5,267,272	11/30/93	Cai et al.			2/14/91
J.L	A18	5,283,578	2/1/94	Ribner et al.			11/16/92
J.L	A19	5,345,406	9/6/94	Williams			8/25/92
J.L	A20	5,430,890	7/4/95	Vogt et al.			11/20/92
J.L	A21	5,442,353	8/15/95	Jackson			10/25/93
J.L	A22	5,451,948	9/19/95	Jekel			2/28/94
J.L	A23	5,500,645	3/19/96	Ribner et al.			3/14/94

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Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
J.L	A24	5,557,642	9/17/96	Williams			11/14/94
J.L	A25	5,712,628	1/27/98	Phillips et al.			8/31/95
J.L	A26	5,742,189	4/21/98	Yoshida et al.		RECEIVED	9/14/95
J.L	A27	5,862,465	1/19/99	Ou		DEC 04 2002	12/30/96
J.L	A28	5,973,601	10/26/99	Campana		Technology Center 2600	12/2/97
J.L	A29	5,758,276	5/26/98	Shirakawa et al.			5/31/96
J.R	A30	5,740,524	4/14/98	Pace et al.			12/14/95
J.L	A31	4,623,926	11/18/86	Sakamoto			11/9/836
J.L	A32	5,341,135	8/23/94	Pearce			4/30/92
J.L	A33	5,241,310	8/31/93	Tiemann			3/2/92
J.L	A34	4,562,591	12/31/85	Stikvoort			2/2/84
J.L	A35	5,243,345	2/21/92	Naus et al.			2/21/92
J.L	A36	5,469,475	11/21/95	Voorman			5/31/91
J.L	A37	4,912,729	3/27/90	Van Rens et al.			12/15/88
J.L	A38	4,627,021	12/2/86	Persoon et al.			3/13/84
J.L	A39	4,692,737	9/8/87	Stikvoort et al.			10/17/86
J.L	A40	4,584,659	4/22/86	Stikvoort			7/5/83
J.L	A41	4,797,845	1/10/89	Stikvoort			12/11/86
J.L	A42	4,604,720	8/5/86	Stikvoort			3/16/84
J.L	A43	5,157,343	10/20/92	Voorman			5/31/91
J.L	A44	5,124,705	7/23/92	Voorman			7/10/91
J.R	A45	4,468,790	8/28/84	Hofelt			2/16/82
J.L	A46	5,859,878	1/12/99	Phillips et al.			8/31/95

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JL	A47	6,323,735	11/27/01	Welland et al.			5/25/00
JL	A48	6,167,245	12/26/00	Welland			5/29/98

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Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	B1	WO 00/22735	4/20/00	Ali			RECEIVED
	B2	GB2233518A	1/9/91	Dedic			DEC 04 2002
	B3	0643477A2	3/15/95	Hulkko et al.			Technology Center 2600
	B4	WO 00/11794	3/2/00	Moore et al.			
	B5	WO 00/01074	1/6/00	Van Der Zwan et al.			
	B6	WO 99/22456	5/6/99	Grenabo			10/27/98

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Exam. Init.	Ref. Des.	Citation
	C1	Stephen Jantzi et al., "Quadrature Bandpass ΔΣ Modulation for Digital Radio," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 1935-1950.
	C2	Stephen Jantzi et al, "A Complex Bandpass ΔΣ Converter For Digital Radio," ISCAS, May/June 1994, pp. 453-456.
	C3	"Analog Devices Delivers World's First Open Market GSM Direct Conversion Radio Chipset," Analog Devices Corporate Information Press Release, http://contentanalog.com/pressrelease/prdisplay/0,1622,102,00.html , September 13, 1999, pp. 1-4.

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P A T E N T & T R A D E M A R K O F F I C E
I N F O R M A T I O N D I S C L O S U R E S T A T E M E N T
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	C4	Data Sheet, CX74017, "RF Transceiver for Single, Dual, or Tri-Band GSM/GPRS Applications," Conexant, January 2, 2001, pp. 1-16.	Technology Center 2800
	C5	Jacques C. Rudell et al, "A 1.9-GHz Wide-Band IF Double Conversion CMOS Receiver for Cordless Telephone Applications," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 2071-2088.	
	C6	Jan Crols et al., "Low-IF Topologies for High-Performance Analog Front Ends of Fully Integrated Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pp. 269-282.	
	C7	Jacques C. Rudell et al., "Recent Developments In High Integration Multi-Standard CMOS Transceiver for Personal Communication Systems," invited paper at the 1998 International Symposium on Low Power Electronics, Monterey, California, 6 pgs.	
	C8	Asad Abidi, "CMOS Wireless Transceivers: The New Wave," IEEE Communications Magazine, August 1999, pp. 119-124.	
	C9	Data Sheet, UAA3535HL, "Low Power GSM/DCS/PCS Multi-band Transceiver," Philips Semiconductors, February 17, 2000, pp. 1-24.	
	C10	Stephen Jantzi et al., "FP 13.5: A Quadrature Bandpass ΔΣ Modulator for Digital Radio," Digest of Technical Papers, 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, pp. 216-217, 460.	
	C11	S. A. Jantzi et al., "The Effects of Mismatch In Complex Bandpass ΔΣ Modulators," IEEE, 1996, pp. 227-230.	
	C12	Qiuting Huang, "CMOS RF Design-The Low Power Dimension," IEEE 2000 Custom Integrated Circuits Conference, pp. 161-166.	
	C13	Paolo Orsatti et al., "A 20-mA-Receive, 55-mA-Transmit, Single-Chip GSM Transceiver in 0.25-μm CMOS," IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, pp. 1869-1880.	
	C14	Qiuting Huang et al., "The Impact of Scaling Down to Deep Submicron on CMOS RF Circuits," IEEE Journal of Solid-State Circuits, Vol. 33, No. 7, July 1998, pp. 1023-1036.	
	C15	Behzad Razavi, "Design Considerations for Direct-Conversion Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 44, No. 6, June 1997, pp. 428-435.	

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Exam. Init.	Ref. Des.	Citation
	C16	Farbod Behbahani et al., "CMOS Mixers and Polyphase Filters for Large Image Rejection," IEEE Journal of Solid-State Circuits, Vol. 36, No. 6, June 2001, pp. 873-887.
	C17	Jan Crols et al., "A Single-Chip 900 MHz CMOS Receiver Front-End With A High Performance Low-IF Topology," IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, pp. 1483-1492.
	C18	Analog Devices, Single-Chip Direct-Conversion GSM/GPRS/EDGE RFIC, Othello One, www.analog.com , 2 pgs.
	C19	Analog Devices, AD6523/AD6524, GSM Direct Conversion Radio Chip Set, www.analog.com , 2 pgs.
	C20	Analog Devices, GSM 3 V Transceiver IF Subsystem, AD6432, www.analog.com , pp. 1-20.
	C21	Hitachi, "RF Transceiver IC For GSM And PCN Dual Band Cellular Systems," HD155121F, ADE-207-265(Z), 1 st Edition, November 1998, pp. 1-56.
	C22	Analog Devices, AD7002 Specification, LC2MOS, GSM Baseband I/O Port, Rev. B, 1997, pp. 1-16.
	C23	Analog Devices, AD20msp415, GSM/DCS1800/PCS1900, Baseband Processing Chipset, Rev. O, 1997, pp. 1-7.
	C24	Kwientus et al., "A Single-Chip Universal Digital Satellite Receiver With 480-MHz IF Input," IEEE Journal of Solid-State Circuits, Vol. 34, No. 11, November 1999, pp. 1634-1646.
	C25	Minnis et al., "A Low-If Polyphase Receiver For GSM Using Log-Domain Signal Processing," IEEE Radio Frequency Integrated Circuits Symposium, 2000, pp. 83-86.
	C26	Atkinson et al., "A Novel Approach To Direct Conversion RF Receivers For TDMA Applications," Analog Devices, 1999, pp. 1-5.
	C27	Crochiere et al., "Optimum FIR Digital Filter Implementations For Decimation, Interpolation, And Narrow-Band Filtering," IEEE Transactions On Acoustics, Speech, And Signal Processing, Vol. ASSP-23, No. 5, October 1975, pp. 444-456.
	C28	Hogenauer, "An Economical Class Of Digital Filters For Decimation And Interpolation," IEEE, 1981, pp. 155-162.
	C29	Brandt et al., "A Low-Power, Area-Efficient Digital Filter For Decimation And Interpolation," IEEE Journal Of Solid-State Circuits, Vol. 29, No. 6, June 1994, pp. 679-687.

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Exam. Init.	Ref. Des.	Citation	DEC 04 2002
	C30	Philips Semiconductors, "uaa3535-Low-Power GSM GPRS Triple-Band Near-Zero-IF Transceiver," October 1999, 4 pgs.	Technology Center 2800
	C31	D'Avella et al., "An Adaptive MLSE Receiver For TDMA Digital Mobile Radio," IEEE Journal On Selected Areas In Communications, Vol. 7, No.1, January 1989, pp. 122-129.	
	C32	Razavi, "CMOS RF Receiver Design For Wireless LAN Applications," IEEE, 1999, pp. 275-280.	
	C33	Lucent Technologies, "W3020 GSM Multiband RF Transceiver," Advance Data Sheet, December 1999, pp. 1-44.	
	C34	Lucent Technologies, "DSP1620 Digital Signal Processor," Data Sheet, June 1998, pp. 1-178.	
	C35	Steyaert et al., "A 2-V CMOS Cellular Transceiver Front-End," IEEE Journal of Solid-State Circuits, Vol. 35, No. 12, December 2000, pp. 1895-1907.	
	C36	Paulus et al., "A CMOS IF Transceiver With Reduced Analog Complexity," IEEE Journal Of Solid-State Circuits, Vol. 33, No. 12, December 1998, pp. 2154-2159.	
	C37	Analog Devices, "Analog Devices Delivers World's First Open Market GSM Direct Conversion Radio Chipset," November 1999, 4 pgs.	
	C38	"Digest Of Technical Papers," 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, 5 pgs.	
	C39	RF Micro Devices, RF2968, Product Description, Blue Tooth Transceiver, Rev A19, pp. 11-199-H-222.	
	C40	Texas Instruments, TRF6901, "Single Chip RF Transceiver," March 2002, pp. 1-29.	
	C41	Texas Instruments, TRF6900A, "Single Chip RF Transceiver," September 2001, pp. 1-34.	
	C42	Texas Instruments, TRF6900, "Single Chip RF Transceiver, October 1999, pp. 1-32.	
	C43	Philips Semiconductor, "Bluetooth RF Transceiver," Data Sheet, UAA3558, December 21, 2000, pp. 1-5.	
	C44	Philips Semiconductor, "Image Reject 1 800 MHz Transceiver For DECT Applications," Data Sheet, UAA2067G, October 22, 1996, pp. 1-24.	

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	C45	Philips Semiconductor, "Analog Cordless Telephone IC," Data Sheet, UAA2062, August 10, 2000, pp. 1-40.	Technology Center 2600
	C46	Philips Semiconductor, "900 MHz Analog Cordless Telephone IC," Data Sheet, UAA3515A, December 12, 2001, pp. 1-44.	
	C47	Philips Semiconductor, "Low Voltage IF I/Q Transceiver," Data Sheet, SA1638, September 3, 1997, pp. 1-26.	
	C48	Texas Instruments, "TCS2100 GPRS Chipset Solution," Product Bulletin, 2001, 4 pgs.	
	C49	Fague, "Othello: A New Direct-Conversion Radio Chip Set Eliminates IF Stages," Analog Dialogue 33-10, 1999, pp. 1-3.	
	C50	Analog Devices, AD6523/AD6524, "GSM Direct Conversion Radio Chip Set," 1999, 2 pgs.	
	C51	Lucent Technologies, "Lucent CSP1089 GSM Conversion Signal Processor For Cellular Handset And Modem Applications," Product Brief, February 2001, 2 pgs.	
	C52	Lucent Technologies, "Lucent CSP1099 GSM Conversion Signal Processor For Cellular Handset And Modem Applications," Product Brief, February 2001, 2 pgs.	
	C53	Lucent Technologies, "Trident," Product Brief, February 2001, 2 pgs.	
	C54	Ericsson, "RF Transceiver Circuit For The Digital Enhanced Cordless Telecommunications (DECT) System," PBL40215, January 2001, pp. 1-22.	
	C55	Micro Linear, "ML2712 2.4GHz Transceiver," Datasheet, August 2001, pp. 1-21.	
	C56	Analog Devices, "GSM/GPRS/DCS1800.PCS1900 SoftFone Baseband Chipset," AD20msp430, 2000, 2 pgs.	
	C57	RF Micro Devices, "Polaris Total Radio Solution," Press Release, 2002, 1 pg.	
	C58	Tuttle, "Introduction To Wireless Receiver Design," Tutorial, 2002, pp. 2-58.	
	C59	Rael et al., "Design Methodology Used In A Single-Chip CMOS 900 MHz Spread-Spectrum Wireless Transceiver," 35 th Design Automation Conference, June 1998, 6 pgs.	
	C60	Troster et al., "An Interpolative Bandpass Converter On A 1.2-μm BiCMOS Analog/Digital Array," IEEE Journal Of Solid-State Circuits, Vol. 28, No. 4, April 1993, pp. 471-477.	

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	C61	Schreier et al., "Decimation For Bandpass Sigma-Delta Analog-To-Digital Conversion," IEEE, 1990, pp. 1801-1804.
	C62	Shoaei et al., "Optimal (Bandpass) Continuous-Time $\Delta\Sigma$ Modulator," pp. 489-492.
	C63	Schreier et al., "Bandpass Sigma-Delta Modulation," Electronics Letters, Vol. 25, no. 23, November 9, 1989, pp. 1560-1561.
	C64	Jantzi et al., "Bandpass Sigma-Delta Analog-To-Digital Conversion," IEEE Transactions On Circuits And Systems, Vol. 38, No. 11, November 1991, pp. 1406-1409.
	C65	Crols et al., "An Analog Integrated Polyphase Filter For A High Performance Low-IF Receiver," Symposium On VLSI Circuits Digest Of Technical Papers, 1995, pp. 87-88.
	C66	Aziz et al., "Performance Of Complex Noise Transfer Functions In Bandpass And Multi Band Sigma Delta Systems," IEEE, 1995, pp. 641-644.
	C67	Jantzi, "A Fourth-Order Bandpass Sigma-Delta Modulator," IEEE Journal Of Solid-State Circuits, Vol. 28, No. 3, March 1993, pp. 282-291.
	C68	Liu et al., "Switched-Capacitor Implementation Of Complex Filters," IEEE International Symposium On Circuits And Systems, Vol. 3, 1986, 5 pgs.
	C69	Sedra et al., "Complex Analog Bandpass Filters Designed By Linearly Shifting Real Low-Pass Prototypes," IEEE International Symposium On Circuits And Systems, Vol. 3, 1985, 5 pgs.
	C70	Thurston et al., "Bandpass Implementation Of The Sigma-Delta A-D Conversion Technique," International Conference On Analogue To Digital And Digital To Analogue Conversion, September 1991, 7 pgs.
	C71	Rudell, et al., "Second Generation Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 9 (June 1996)
	C72	Cho, et al., "Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 26 (June 1996)
	C73	Copending U.S. Patent Application Serial No. 09/821,342, filed March 29, 2001, "Partitioned Radio-Frequency Apparatus And Associated Method" (SILA:072)
	C74	Copending U.S. Patent Application Serial No. 09/821,340, filed March 29, 2001, "Digital Interface In Radio-Frequency Apparatus And Associated Methods" (SILA:073)

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	C75	Copending U.S. Patent Application Serial No. 10/075,094, filed February 13, 2002, "Radio-Frequency Communication Apparatus And Associated Methods" (Sila:074)
	C76	Copending U.S. Patent Application Serial No. 10/075,098, filed February 13, 2002, "Apparatus And Methods For Generating Radio Frequencies In Communication Circuitry" (Sila:075)
	C77	Copending U.S. Patent Application Serial No. 10/075,122, filed February 12, 2002, "Digital Architecture For Radio-Frequency Apparatus And Associated Methods" (Sila:078)
	C78	Copending U.S. Patent Application Serial No. 10/083,633, filed February 26, 2002, "Apparatus And Methods For Calibrating Signal-Processing Circuitry" (Sila:080)
	C79	Copending U.S. Patent Application Serial No. 10/081,121, filed February 22, 2002, "Calibrated Low-Noise Current And Voltage References And Associated Methods" (Sila:095)
	C80	Copending U.S. Patent Application Serial No. 10/074,591, filed February 13, 2002, "Apparatus For Generating Multiple Radio Frequencies In Communication Circuitry And Associated Methods" (Sila:096)
	C81	Copending U.S. Patent Application Serial No. 10/075,099, filed February 12, 2002, "Notch Filter For DC Offset Reduction In Radio-Frequency Apparatus And Associated Methods" (Sila:097)
	C82	Copending U.S. Patent Application Serial No. 10/074,676, filed February 12, 2002, "DC Offset Reduction In Radio-Frequency Apparatus And Associated Methods" (Sila:098)
	C83	Copending U.S. Patent Application Serial No. 10/079,058, filed February 19, 2002, "Apparatus And Methods For Output Buffer Circuitry With Constant Output Power In Radio-Frequency Circuitry" (Sila:099)
	C84	Copending U.S. Patent Application Serial No. 10/081,730, filed February 22, 2002, "Method And Apparatus For Synthesizing High-Frequency Signals For Wireless Communications" (Sila:106)
	C85	Copending U.S. Patent Application Serial No. 10/079,057, filed February 19, 2002, "Apparatus And Method For Front-End Circuitry In Radio-Frequency Apparatus" (Sila:107)
	C86	Allen, "Complex Analog Filters Obtained From Shifted Lowpass Prototypes," September 1985, 118 pgs.

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See Pages 3-10**Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)**

Exam. Init.	Ref. Des.	Citation
	C87	Motorola Communications Semiconductor Product Division, "A 1.9 GHz Chipset For PCS Applications," Microwave Journal, No. 6, June 1995, 3 pgs.
	C88	Search Report for PCT/US02/00896; October 4, 2002; 7 pgs.
	C89	Copending U.S. Patent Application Serial No. 09/708,339, filed November 8, 2000, "Method And Apparatus For Operating A PLL With A Phase Detector/Sample Hold Circuit For Synthesizing High-Frequency Signals For Wireless Communications" (Sila:035C1)
	C90	Copending U.S. Patent Application Serial No. 09/999,702, filed October 31, 2001, "Method And Apparatus For Synthesizing Dual Band High-Frequency Signals For Wireless Communications" (Sila:060C1)
	C91	Search Report for PCT/US02/00895; November 11, 2002; 6 pgs.

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list of Patents and Publications for Applicant's
INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Atty. Docket No. SILA:097	Serial No. 10/075,099
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Applicants TOD PAULUS ET AL.

Filing Date: 2/12/02	Group: 2681
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U.S. Patent Documents See Page 1	Foreign Patent Documents See Page	Other Art See Page
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U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
JL	A49	6,539,066	3/25/03	Heinen			11/10/99
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JL	A51	6,002,925	12/14/99	Vu et al.			3/24/97

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.

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